

# CADENCE Allegro and OrCAD RELEASE 17.2-2016 README for System-in-Package (SiP) and Allegro Package Designer Products

This document provides detailed information for the System-in-Package (SiP) and Allegro® Package Designer (APD) products. Refer to the *Cadence Allegro and OrCAD Release 17.2-2016 Readme* for general release information.

## ***SiP/Allegro Package Designer Product Compatibility Information***

The System-in-Package (SiP), Allegro Package Designer (APD) and Cadence® 3D Design Viewer products are installed from the Cadence Allegro® and OrCAD® 17.2-2016 Install DVD.

Virtuoso® SiP Architect XL provides an integrated flow with the Virtuoso Floorplan, Layout, and Simulation environment. Virtuoso products are installed from the Virtuoso (IC) CD ROM image.

For Cadence Allegro and OrCAD 17.2-2016, the compatibility of Cadence SiP and APD products with OA, Cadence Innovus™ Implementation Systems, and Virtuoso is described below.

### Product Compatibility Details

Product name	Version
Innovus Implementation Systems	INNOVUS 15.20
Virtuoso (OA database)	6.1.7-64b.500.2

## SiP 17.2-2016 Computing Platform Support Matrix

The following table presents the supported operating systems for the SiP, APD and Cadence 3D Design Viewer products. For additional information on system requirements, see the *Allegro Platform System Requirements* document.

### Computing Platform Support for SiP 17.2-2016 SiP/APD Products

Product name	Windows 7 (64-bit)	Windows 8 (64-bit)	Windows 10 (64-bit)	2008 R2 Server	2012 Server	Linux RHEL 5.10 (64-bit)	Linux RHEL 6.5 (64-bit)	Linux RHEL 7.1 (64-bit)	SLES/SUS E 11 SP2/SP3( 64-bit)
Cadence SiP Digital Architect XL SIP110	Y	Y	Y	Y	Y	Y	Y	Y	N
Cadence SiP Digital Architect GXL SIP125	N	N	N	N	N	Y	Y	Y	N
Cadence SiP Digital SI XL SIP215	Y	Y	Y	Y	Y	Y	Y	Y	N
Cadence SiP Layout XL SIP225	Y	Y	Y	Y	Y	Y	Y	Y	N
Cadence SiP Layout XL SIP230	Y	Y	Y	Y	Y	Y	Y	Y	N
Virtuoso SiP Architect XL SIP410	N	N	N	N	N	Y	Y	Y	N
Allegro Package Designer L PX4100	Y	Y	Y	Y	Y	Y	Y	Y	Y
SiP Layout XL (RF Structure Editing) SIP630	Y	Y	Y	Y	Y	Y	Y	Y	N
Allegro Package SI L PA7625	Y	Y	Y	Y	Y	Y	Y	Y	Y
Allegro Design Publisher XL PA1220	Y	Y	Y	Y	Y	Y	Y	Y	N
Cadence 3D Design Viewer PA6605	Y	Y	Y	Y	Y	Y	Y	Y	N
Advanced Package Router Option SIGR625	Y	Y	N	N	N	N	N	N	N

#### NOTES:

- (1) Spectre Circuit Simulator is not supported on Windows platforms.
- (2) There may be additional platform limitations depending on the version of Innovus/Virtuoso that you are running. Refer to Cadence Online Support for Innovus/Virtuoso Computing Platforms.

## Allegro and OrCAD (Including ADW) 17.2-2016 SiP/Cadence IO Planner Interoperability

The following table describes the versions of SiP and Cadence IO Planner validated by Cadence.

### SiP/Cadence IO Planner Interoperability

SiP Version	Cadence IO Planner Version	OA Version
17.2-2016	INNOVUS15.20.00	OA 22.50.037

## **Virtuoso SiP Architect Installation and Setup**

Virtuoso Analog Design Environment (ADE) and Virtuoso Schematic Editor must be installed as part of the Virtuoso SiP Architect XL integrated solution. Licenses required for IC 6.1.7-64b.500.2with Allegro and OrCAD (Including ADW) 17.2-2016 are as follows:

<b>Virtuoso SiP Architect on IC 6.1.7 (OA) Release and Allegro and OrCAD (Including ADW) 17.2-2016</b>		
Allegro and OrCAD (Including ADW) Licenses	SIP410	Virtuoso SiP Architect XL
	SIP225	Cadence SiP Layout XL
	SIP630	RF Structure Editing Option
Virtuoso Licenses	95100	Virtuoso® Schematic Editor L
	95200	Virtuoso® Analog Design Environment L
	38500	Virtuoso® Spectre® Simulator (with MMSIM 11.10.507 or later release)
	95300	Virtuoso_Layout_Suite_L

Set the following paths if using Virtuoso IC 6.1.7:

```
set path = (  
$MMSIMHIER/tools/dfII/bin  
$MMSIMHIER/tools/bin  
$ICHIER/tools/bin  
$ICHIER/tools/dfII/bin  
$PCBHIER/tools/bin  
$PCBHIER/tools/dfII/bin  
)
```

where

PCBHIER is Allegro and OrCAD (Including ADW) 17.2-2016 installation

ICHIER is IC617 installation

MMSIMHIER is MMSIM 15.1.0.385 installation (optional)

To run Virtuoso SiP Architect, run *rfwb*

## **Symbol Library and Padstacks**

All library symbols created with previous releases of Allegro Package Designer (APD) and SiP are compatible with this release. Before loading the generic library, be sure that customized component symbols are not in the same directory in which you load the release.

The library is overwritten when you load the generic library. The CAE libraries will not be affected. Before using any of the library symbols, carefully review them to ensure that they meet your physical design criteria.